

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Final Office Action dated February 7, 2008, has been received and its contents carefully reviewed.

Claims 2, 4-6, 10, 17-20, 23 and 25-30 are rejected by the Examiner. Claims 23-30 have been canceled. Claims 2-6, 9-11 and 16-20 remain pending in this application. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, claim 2 is rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Publication No. 2001/0005240 to Takeishi (hereinafter "Takeishi"). Claim 4-6, 10, 17-20, 23 and 25-30 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Takeishi in view of U.S. Patent No. 6,636,281 to Kanatsu (hereinafter "Kanatsu"). Claims 9 and 16 are allowed. Claims 3, 11 and 24 are objected to be being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitation of the base claim and any intervening claims.

Claim 2 is rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Publication No. 2001/0005240 to Takeishi (hereinafter "Takeishi"). This rejection is respectfully traversed and reconsideration is requested.

Claim 2 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a shield cover covering the printed circuit board formed the a variable resistor, wherein the shield cover includes a structure arranged over the variable that is openable for adjusting the common voltage, and further wherein the structure includes a U-shaped opening in the shield cover around the variable resistor, such that the variable resistor is selectively exposable by the structure". Takeishi does not disclose these features.

As shown Figures 3A and 5 of Takeishi, a shield plate 12 protects a liquid crystal module 17 at the outer edge only and does not cover the PCB 13. However, the shield cover in the claimed invention covers the printed circuit board. Also, as shown Figures 3B, 5, 6A-6C, 8A-8D and 10A-10C, opening 31 is formed in PCB 13 and is not formed in shield plate 12. However, the a U-shaped opening in the claimed invention is formed in the shield cover. Thus, the shield

cover, having the above features in the claimed invention, differ from the shield plate 12 in Takeishi. Accordingly, Applicant respectfully submits that claim 2 is allowable over Takeishi.

Claims 4-6, 10, 17-20, 23 and 25-30 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Takeishi in view of U.S. Patent No. 6,636,281 to Kanatsu (hereinafter “Kanatsu”). This rejection is respectfully traversed and reconsideration is requested.

Claim 4 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, “a shield cover covering the printed circuit board formed the a variable resistor; a module connector formed on the printed circuit board; a board connector connected with the module connector; wherein the shield cover includes a structure arranged over the variable resistor that is openable for adjusting the common voltage and an opened part arranged over the module connector and inserted the board connector so as to be connected with the module connector.” None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention.

As described above in claim 2, Takeishi fails to disclose the shield cover as claimed invention. As shown in Figures 4, 9 and 11 of Kanatsu, there are disclosed a main silt 7 to facilitate the bending of the TCP and a sub silt 8 to temporarily bend the TCP and to be returned to a flat state. However, the opened part in the shield cover of the claimed invention is arranged over the module connector and inserted the board connector so as to be connected with the module connector. Thus, an opened part, inserted the board connector, in the claimed invention differ from the main silt 7 and the sub silt 8 in Kanatsu. Accordingly, Applicant respectfully submits that claim 4 and claims 5-6 which depend from claim 4 are allowable over the cited references.

Claim 10 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, that “a shield cover comprises a covering structure over the PCB, the covering structure comprising at least one elastically deformable region; a slit formed in the covering structure over the PCB and inserted a board connector so as to be connected with the PCB; the shield cover further comprising a flap in the covering structure over the variable resistor”. None of the cited references including Takeishi and Kanatsu, singly

or in combination, teaches or suggests at least this feature of the claimed invention. As described above in claim 4, Takeishi and Kanatsu fail to disclose the above features as claimed invention. Accordingly, Applicant respectfully submits that claim 10 and claim 11 which depends from claim 10 are allowable over the cited references.

Claim 17 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, that "a shield cover comprises a slit formed in the shield cover a printed circuit board and inserted a board connector so as to be connected with the PCB ". None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention. As described above in claim 4, Takeishi and Kanatsu fail to disclose the above features as claimed invention. Accordingly, Applicant respectfully submits that claim 17 and claims 19-20 depend from claim 17 are allowable over the cited references.

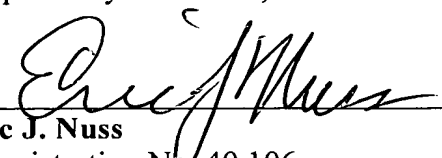
Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

Dated: May 2, 2008

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